

- Instructors:** Chris Hadjicostis Z.-P. Liang
148 CSL 4257 Beckman
chadjic@uiuc.edu z-liang@uiuc.edu
265-8259 244-4023
- TA's:** Yunhui Chu Andreas Ehmann Sung-Hoon Kim Shankar Rao
ychu@uiuc.edu aehmann@uiuc.edu sungkim6@uiuc.edu srrao@uiuc.edu
- Office Hours:** **Instructors:** Wed. 1:00-2:30pm 141 CSL
Thu. 2:30-4:00pm 141 CSL
TAs: Tue. 1:00-2:30pm 330M EL
Wed. 3:00-4:30pm 330M EL
Thu. 12:30-2:00pm 330M EL
- URL:** <http://courses.ece.uiuc.edu/ece310>
Check this web page for up-to-date course information, schedule, and announcements
- Prerequisites:** ECE210 (Analog Signal Processing), or equivalent course on signals and systems
- Homeworks:** Weekly assignments, due Fridays in class
Solutions will be posted on the web
No late homeworks will be accepted
- Exams:** A final exam and three tests on the following dates:
Test 1: Thursday, February 20
Test 2: Thursday, March 20
Test 3: Thursday, April 24
- The three tests will be given at 7:00 – 8:30 PM, to reduce time pressure. However, a lecture will be cancelled for each test on dates listed in the online Course Schedule.
 - The tests will be closed book. However, you may bring one two-sided 8.5"×11" sheet of handwritten notes to each test, in addition to the sheets of notes from the prior tests. You can bring all three sheets of notes to the Final Exam.
- Grading:** Homework 15%
Three Tests 45%
Final Exam 40%
- Required Text:** 1) D.C. Munson, Jr., *ECE 310 Course Notes*, 2002
2) *Matlab, the Student Version*, The MathWorks
Software not required – mounted on all engineering workstations
(<http://www.ews.uiuc.edu>)
- Optional Texts:** Proakis and Manolakis, *Introduction to Digital Signal Processing*,
Prentice-Hall, 3rd. Edition, 1996.
Oppenheim and Schaffer, with Buck, *Discrete-Time Signal Processing*,
Prentice-Hall, 2nd Ed. 1999.
- References:** See web for books on reserve at Grainger Eng. Library